

ABSTRACT

A display error occurs upon contention between writing of pixel data in a GRAM and reading of pixel data representing a scanning line including pixels which correspond to the pixel data above.

Pixel data corresponding to pixels representing a scanning line stored in a latch circuit 3 is displayed on a display panel 8, and when contention occurs between writing of pixel data in a GRAM 2 and reading of pixel data corresponding to pixels representing a scanning line to the latch circuit 3 from the GRAM 2, a control means 4 delays reading of the pixel data corresponding to the pixels representing the scanning line and controls so as to perform reading of the pixel data corresponding to the pixels representing the scanning line to the latch circuit 3 from the GRAM 2 once again.